

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-10 (cancelled)

Claim 11 (currently amended): An integrated circuit comprising:

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an activation circuit to determine whether a supply voltage reaches a predetermined level, said activation circuit including an inverter coupled to the gate of a load transistor, a second transistor coupled to said load transistor and a third transistor coupled between said load transistor and said first second transistor;

a pulse generator to generate pulses to indicate that a the supply voltage is ramping up and to terminate the generation of the pulses after the supply voltage reaches a predetermined level; and

a feedback path to provide an output of said pulse generator to said activation circuit, the activation circuit to latch a high signal in response to a low signal on said feedback path.

Claim 12 (original): The integrated circuit of claim 11 further including a logic functionality to emulate logic that is difficult to trigger and to determine whether the supply voltage has reached a level sufficient to trigger the difficult to trigger logic.

Claim 13 (original): The integrated circuit of claim 11 including a level detector that detects when a voltage is above at least two transistor threshold voltages, said level detector operative to control said pulse generator.

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Claims 14-15 (cancelled)

Claim 16 (previously presented): The integrated circuit of claim 11 including a pair of transistors that must both conduct in order to generate said pulses.

Claim 17 (original): The integrated circuit of claim 16 including a capacitor circuit to enable the supply voltage to reach a designated output level.

Claim 18 (original): The integrated circuit of claim 17 including a hysteresis sense stage coupled to said capacitor circuit.

Claim 19 (cancelled)

Claim 20 (original): The integrated circuit of claim 11 including a circuit to latch the pulse generator in response to the supply voltage being in a first state.

Claims 21-30 (cancelled)

Claims 31-43 (cancelled)

Claim 44 (New): The integrated circuit of claim 11 wherein the load transistor is coupled to the supply voltage.

Claim 45 (New): The integrated circuit of claim 11 further including a second activation circuit to determine whether the supply voltage reaches the predetermined level.

Claim 46 (New): The integrated circuit of claim 17 wherein the capacitor circuit includes a first capacitor coupled to the supply voltage and a second capacitor coupled to the first capacitor through a transistor.
